ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

10/26/04

Title of Invention	SYSTEM AND METHOD FOR CONFIGURING A RECONFIGURABLE SYSTEM

Application Number: 10/055241

Confirmation Number: 8631

First Named Applicant: Joseph Peck

Attorney Docket Number: 5150-63500

Art Unit: ~~2123~~ 2191

Examiner: Unknown Unknown Mary Steelman

Search string: (5541849 or 5737235 or 5638299 or 5309556 or 6064409).pn

US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
MS	1	5541849	1996-06-30	Rostoker et al.			
MS	2	5737235	1998-04-07	Kean et al.			
MS	3	5638299	1997-06-10	Miller			
MS	4	5309556	1994-05-03 ✓	Sismilich			
MS	5	6064409	2000-05-16	Thomsen et al.			

Signature

Examiner Name	Date
M. Steelman	6.22.05

Form PTO-1449 (modified)
List of Patents and Publications
For Applicant's Information
Disclosure Statement
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OCT 06 2003 JC24 PATENT & TRADEMARK OFFICE

ATTY. DOCKET NO: 5150-63500

SERIAL NO: 10/055,241

APPLICANT: Peck et al.

FILING DATE: October 29, 2001

GROUP: 2191 ~~2123~~

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U.S. PATENT DOCUMENTS

EXAM. INITIALS	REF. DES	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
MX	A1	4,849,880	7/18/89	Bhaskar et al.			
MX	A2	4,901,221	2/13/90	Kodosky et al.			
MX	A3	5,109,504	4/28/92	Littleton			
MX	A4	5,437,464	8/1/95	Terasima et al.			
MX	A5	5,497,498	3/5/96	Taylor			
MX	A6	5,535,342	7/9/96	Taylor			
MX	A7	5,555,201	09/10/1996	Dangelo et al.			
MX	A8	5,566,295	10/15/96	Cypher et al.			
MX	A9	5,583,749	12/10/96	Tredennick et al.			
MX	A10	5,603,043	02/11/1997	Taylor et al.			
duplicate	~~A11~~	~~5,603,043~~	~~2/11/97~~	~~Taylor et al.~~			
MX	A12	5,652,875	7/29/97	Taylor			
MX	A13	5,684,980	11/4/97	Casselman			
MX	A14	5,732,277	03/24/1998	Kodosky et al.			
MX	A15	5,784,275	7/21/98	Sojoodi et al.			
MX	A16	5,847,955	12/98	Mitchell et al.			
MX	A17	6,219,628	04/2001	Kodosky et al.			
MX	A18	6,226,776	05/01/2001	Panchul et al.			
MX	A19	6,230,307	05/082001	Davis et al.			
MX	A20	6,311,149	10/30/2001	Ryan et al.			

FOREIGN PATENT DOCUMENTS

EXAM. INITIALS	REF. DES	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	TRANSLATION. YES/NO
MX	A21	WO 94 10627 A	5/11/94	PCT			
MX	A22	WO 94 15311 A	7/7/94	PCT			
MX	A23	WO 96 14618 A	5/17/96	PCT			

Form PTO-1449 (modified) List of Patents and Publications For Applicant's Information Disclosure Statement (Use several sheets if necessary)	ATTY. DOCKET NO: 5150-63500 OCT 06 2003 JC24 PATENT & TRADEMARK OFFICE	SERIAL NO: 10/055,241 RECEIVED OCT 09 2003 Technology Center 2100
	APPLICANT: Peck et al.	
	FILING DATE: October 29, 2001	GROUP: 2191 (2123)

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

	A24	Douglas, Bruce D., Ph.D., "Custom Embedded Communication Protocols, Conference Proceedings of Embedded Systems Conference East, March 10-12, 1997, Boston, Massachusetts, pp. 637-657.
	A25	Labrosse, Jean, "Inside Real-Time Kernels," Conference Proceedings of Embedded Systems Conference East, March 10-12, 1997, Boston, Massachusetts, pp. 205-214.
	A26	Labrosse, Jean, "Designing with Real-Time Kernels," Conference Proceedings of Embedded Systems Conference East, March 10-12, 1997, Boston, Massachusetts, pp. 379-389.
	A27	Operating Systems, 2nd Ed. By Williams Stallings, Prentice Hall, 1995, "Multiprocessor and Real-Time Scheduling", pp. 394-415.
	A28	International Search Report for PCT/US 98/10916 dated 9/17/98.
	A29	"LabVIEW USER MANUAL for Windows", National Instruments Co., pp. 4/1-21.
	A30	Beguelin et al., "Visualization and Debugging in a Heterogenous Environment," IEEE, pp. 88-95.
	A31	Jamal et al., "The Applicability of the Visual Programming Language LabView to Large Real-World Application," IEEE, pp. 99-106.
	A32	Per-Arne Wiberg, "Graphical Programming of Time-Deterministic Real-Time Systems," IEEE, pp. 173-180.
	A33	XP0002087124 Xiao-Yu, et al., "Software Environment for WASMII: A Data-Driven Machine with a Virtual Harware," Field Programmable Logic Architectures, Synthesis and Applications, 4th International Workshop on Field-Programmable Logic and Applications, FPL '94 Proceedings, Berlin Germany, 1994.
	A34	XP000554820 Edwards, et al., "Software acceleration using programmable hardware devices," IEEE Proceedings: Computers and Digital Techniques, Vol. 143, No. 1, January 1996, pp. 55-63.
	A35	XP000380758 Lesser, et al., "High Level Synthesis and Generating FPGAs with the BEDROC System," Journal of VLSI Signal Processing, Vol. 6, No. 2, August 1993, pp. 191-214.
	A36	International Search Report for PCT/US 98/13040 mailed 12/22/98.
	A37	Ade, M; Lauwereins, R; Peperstraete, J.A.; Hardware-Software Codesign with GRAPE, Proceedings of the Sixth IEEE International Workshop on Rapid System Prototyping, pp. 40-47, June 1995.
	A38	De Coster, GRAPE-II: An Introduction [online]. Automatic Control and Computer Architectures Department. Katholieke Universiteit Leuven, Belgium, February 22, 1996 [retrieved October 6, 1999] Retrieved from the Internet @ http://www.esat.kuleuven.ac.be/acca.

Form PTO-1449 (modified)
List of Patents and Publications
For Applicant's Information
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APPLICANT: Peck et al.

FILING DATE: October 29, 2001

GROUP: ~~2123~~ 2191

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

MD	A39	Wenban et al., A Software Development System for FPGA-based Data Acquisition Systems, Proceedings of the IEEE Symposium on FPGAs for Custom Computing Machines, pp. 28-37, April 1996.
	A40	~~Collamati et al. "Induction Machine Stator Fault On-line Diagnosis Based on LabVIEW Environment", Mediterranean Electrotechnical Conference, Vol. 1, pg. 495-498, May~~ 1996.
	A41	~~Spoelder et al., "Virtual Instrumentation: A Survey of Standards and Their Interrelation",~~ Proc. ~~IEEE Instr. And Measurement Tech. Conf., Vol. 1, pp. 676-681, May 1997~~.
	A42	~~Srinivasan et al., "LabVIEW program Design for On-Line Data Acquisition and Predictive Maintenance", Proc. Of the 30th Southeastern Symp. On System Theory, pp. 520-524,~~ March 1998.
	A43	~~Wahidanabanu et al., "Virtual Instrumentation with Graphical Programming for Enhanced Detection and Monitoring of Partial Discharges", Proc. Electrical Insulation Conf. 1997, pp. 291-296, September 1997~~.
	A44	~~Fountain: "Software advances in measurement and instrumentation"; IEE Coll. Software Instrumentation, pp. 4/1 to 4/45.~~

XAMINER: M. Shechtman DATE CONSIDERED: 6·22·05

XAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through itation if not in conformance and not considered. Include copy of this form with next communication to the patent owner.

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	APPLICANT: Joseph E. Peck	
	FILING DATE: October 29, 2001	GROUP: ~~2123~~ 2191

OIPE JC133 NOV 0 3 2004 PATENT & TRADEMARK OFFICE

U.S. PATENT DOCUMENTS

EXAM. INITIALS	REF. DES	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE

FOREIGN PATENT DOCUMENTS

EXAM. INITIALS	REF. DES	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	TRANSLATION YES/NO
MK	C1	DE 692 32 869 T2	Sept. 4, 2003	Germany			YES
MK	C2	DE 42 05 524 A1	Aug. 27, 1992	Germany			YES

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

MK	C3	Lauwereins, R; Engels, M; Ade, M; Peperstraette, J; Grape-II: A System-Level Prototyping Environment for DSP Applications, Computer, Vol. 28, Issue 2, pp. 35-43, 2/95.
MK	C4	Lysaght, P; Stockwood, J; A Simulation Tool for Dynamically Reconfigurable Field Programmable Gate Arrays, IEEE Transactions on Very Large Scale Integration Systems, Vol. 4, Issue 3, pp. 381-390, 9/96.
MK	C5	Petronino et al., An FPGA-based Data Acquisition System for a 95 GHz. W-band Radar, IEEE International Conference on Acoustics, Speech and Signal Processing, Vol. 5, pp. 4105-4108, April 1997.
MK	C6	Boulay et al., A High Throughput Controller for a 256-Channel Cardiac Potential Overlapping System, Canadian Conference on Electrical and Computer Engineering, Vol. 1, pp. 539-542, September 1995.

XAMINER: M. Steelman | DATE CONSIDERED: 6-22-05

XAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through itation if not in conformance and not considered. Include copy of this form with next communication to the patent owner.

Form PTO-1449 (modified) List of Patents and Publications For Applicant's Information Disclosure Statement (Use several sheets if necessary)	ATTY. DOCKET NO: 5150-61502	SERIAL NO: 10/058,150
	APPLICANT: Odom et al.	
	FILING DATE: October 29, 2001	GROUP: 2191 ~~2123~~

OIPE JC133 NOV 0 3 2004 PATENT & TRADEMARK OFFICE

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

MA	C7	Levy, M., "DSP Design Tools Target FPGAs," www.reed-electronics.com/ednmag/archives/1996/062096/13df2.htm, June 20, 1996.
MA	C8	Heimdahl, M.P.E., Keenan, D.J., "Generating Code from Hierarchical State-Based Requirements," Proceedings of IEEE International Symposium on Requirements Engineering (RE'97)," January 1997.
MA	C9	Integrated Systems, Inc., "MATRIXx Product Family Technical Specifications," Product Manual, Copyright 1995.
MA	C10	Pauer, E.K., "Multiprocessor System Development for High Performance Signal Processing Applications," Proceedings of the 1997 IEEE Int. Workshop on Rapid System Prototyping (RSP)," Copyright 1997.
MA	C11	Drusinsky, D., "Extended State Diagrams and Reactive Systems," Dr. Dobb's Journal, October 1994.
	~~C12~~	~~Chen, X., Ling, X., Amano, H., "Software Environment for WASMII: a Data Driven Machine with a Virtual Hardware," Proceedings of 4th Workshop on Field-Programmable Logic and Applications (FPL '94), pages 208-219, Springer-Verlag, September 1994.~~

XAMINER:	DATE CONSIDERED: 6·22·05

XAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through itation if not in conformance and not considered. Include copy of this form with next communication to the patent owner.